1/29

**CURRICULUM VITAE:** 

David Lorge Parnas, P.Eng.

Software: Design, Documentation, Quality Assurance

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**PERSONAL:** 

Born: 10 February 1941 Citizen of Canada

## AREAS OF INTEREST:

The term "software engineering" was coined to suggest that those who design and build software should work with the professional knowledge and discipline that is expected of engineers in other fields. Engineers have been taught a well developed set of problem-solving skills, analytical methods, mathematics, and scientific knowledge that allows them to apply sound methods to the design of trustworthy useful devices. They need not rely exclusively on intuition and natural ability. Software development is now supported by a similar body of knowledge but not by an appropriate educational and licensing system. Expanding the body of knowledge, and assisting in the development of appropriate educational and licensing systems, are my professional goals.

I have been studying the problems of software development since 1965. By combining system construction, consultation, and academic research I have been able to develop principles and methods that are both of academic interest and applicable to real-world problems. My work, much of which is listed below, has received wide recognition. It includes a number of papers on modularity, specifications, documentation and concurrency that are considered fundamental contributions.

It is my intent to continue confronting the problems of software development, taking time to identify the fundamental causes of those problems, experimenting with better methods and ideas, writing papers, and teaching what I have learned. In designing systems, developing theories, and writing papers, I am challenged by Albert Einstein's, "Everything should be as simple as possible, but not simpler".

# **EDUCATION:**

1954-1957 Bronx High School of Science, New York, New York

1957-1961 B.S. Electrical Engineering (honors), Carnegie Institute of Technology, Pittsburgh, Pa.

Fall 1961 Graduate student (Math) at University of Michigan, Ann Arbor, MI; "A" average.

1964 M.S. Electrical Engineering Carnegie Institute of Technology

1965 Ph.D., Electrical Engineering, Systems & Communications Sciences, Carnegie-Mellon University

LICENSE:

Professional Engineer in the Province of Ontario

# **PROFESSIONAL EXPERIENCE:**

- 1960-1965 Teaching, Electrical Engineering, Carnegie Institute of Technology.
- 1965-1966 Assistant Professor, Computer Science, Univ. of Maryland, College Park, Md.
- 1965-1969 Assistant Professor of Computer Science and Electrical Engineering, Carnegie-Mellon University, Pittsburgh, Pa. USA
- 1969-1970 Advisor to Philips-Electrologica, Apeldoorn, The Netherlands
- Summer 1970 Visiting Scientist, Lecturer, MBLE (Philips) Research Laboratory Brussels, Belgium.
- 1970-1973 Assoc. Professor of Computer Science, Carnegie Mellon Univ., Pittsburgh, PA., USA
- 1972-1979 Consultant to the Naval Research Laboratory.
- 1973-1976 Professor and Head of the Research Group on Operating Systems at the Technical University of Darmstadt, Germany.
- 1976-1982 Professor of Computer Science at University of North Carolina at Chapel Hill.
- 1979-1982 Visiting Scientist at IBM, Federal Systems Division (half-time), Bethesda MD.
- 1979-1982 Half-time employee, Head of the Software Engineering Research Section, US Naval Research Laboratory, Washington D.C
- 1982-1986 Professor of Computer Science, University of Victoria, British Columbia, Canada.
- 1982-1986 Investigator, Software Cost Reduction Project, US Naval Research Laboratory, Washington D.C. USA
- 1986 1991 Professor of Computer Science at Queen's, Kingston, Canada.
- 1991- 1997 Professor of Electrical and Computer Engineering, McMaster University, Canada
- 1995 2000 NSERC/Bell Industrial Research Chair in Software Engineering at McMaster
- 1998 2002 Director of the Software Engineering Programme at McMaster University,
- 1998 2006 Professor of Computing and Software, McMaster (now Professor Emeritus)
- 2001 2001 Acting Chair, Department of Computing and Software (1.10 → 30 12)
- 2002- 2008 Director of the Software Quality Research Laboratory, Professor of Software Engineering, SFI Fellow, University of Limerick, Ireland (→ 31.03.2008) (now Professor Emeritus)

#### **OTHER OFFICES HELD:**

President, Science for Peace. (1990 - 1993)

Deputy Chairman, Canadian Pugwash Group. (1991 - 1993)

## **HONOURS:**

- 1979: "Best Paper" award of the Association for Computing Machinery (for 60).
- 1986: "Eminent Lecturer" Institution of Engineers, Australia
- 1986: Honorary Doctor of Technical Science, ("Doktors der technischen Wissenschaften ehrenhalber"), granted by the Federal Institute of Technology (ETH) in Zurich Switzerland,
- 1987: Norbert Wiener Award for Professional and Social Responsibility from Computing Professionals for Social Responsibility
- 1991: "Most Influential Paper from ICSE 3" awarded at ICSE 13 (13th International Conference on Software Engineering), (for 60)
- 1992: Elected a Fellow of the Royal Society of Canada
- 1994: Elected a Fellow of the Association for Computing Machinery
- 1995: "Best Paper from ICSE 7" (with David Weiss and Paul Clements) awarded at the 17<sup>th</sup> International Conference on Software Engineering.(for 79)
- 1996: Doctorate Honoris Causa, granted by Catholic University of Louvain (Belgium)
- 1998: "Outstanding Research Award" of the Association for Computing Machinery Special Interest Group on Software Engineering
- 1998: Merit Award, Carnegie Mellon University Alumni Association.
- 1999: "Practical Visionary Award" presented at a symposium in honour of the late Dr. Harlan Mills at the International Conference on Software Engineering in Los Angeles Ca.
- 1999: "Component and Object Technology Award" "TOOLS 99" Santa Barbara Ca.
- 2001: Elected a Fellow of the Canadian Academy of Engineering
- 2001: Winner of the FifF (Forum Informatiker für Frieden und Verantwortung) Prize
- 2006: Elected a Member of the Royal Irish Academy
- 2006 Honorary Professor at Ji Lin University, China
- 2007 Co-winner of IEEE Computer Society 60<sup>th</sup> Anniversary Award (one-time award shared with Prof. Maurice Wilkes of Cambridge University)
- 2008 Dottore Honoris Causa in Scienza Informatiche, granted by L'Universita' della Svizzera Italiana (Lugano, Switzerland).
- 2008 Honorary Professor at Chan Chung University, China
- 2008 Fellow der Gesellschaft für Informatik e.V. (GI)
- 2008 ACM SIGSOFT Impact Paper Award (for 79)
- 2009 Fellow of the Institute of Electrical and Electronics Engineers (IEEE)
- 2010 ACM SIGSOFT Impact Paper Award (for 166)
- 2011 Ehrendoktorwürde der Technische Universität Wien (Dr. Tech.H.C.), Vienna Austria
- 2011 Outstanding Achievement Award, WORLDCOMP'11, 18.07.2011, Las Vegas Nevada.
- 2015 ACM SIGOPS Hall of Fame Award(for 26)

# **PUBLICATIONS:**

- 1. Parnas, D.L., "Kirchhoff's Laws", Programmed Text, Electrical Engineering Department, Carnegie Institute of Technology, Pittsburgh PA., 1960, 43 pgs.
- 2. Parnas, D.L., "Elementary Calculus: Programmed Study Material", Computation Center, Carnegie-Mellon University, Pittsburgh PA., 1962.
- 3. Strauss, J.C., Parnas, D.L., Snelsire, R., Wallach, Y., "A Design-Emphasis Problem Solving Experience", Electrical Engineering Department, Carnegie Institute of Technology, Pittsburgh PA., 1963, 129 pgs.
- 4. Parnas, D.L., "System Function Description-Algol", (Ph.D. Thesis), Computation Center, Carnegie-Mellon University, Pittsburgh PA., February 1965, 100 pgs.
- 5. Parnas, D.L., "A Language for Describing the Functions of Synchronous Systems", *Communications of the ACM*, 9, 2, February 1966, pp. 72-75.
- 6. Parnas, D.L., "On the Use of the Computer in Engineering Education without a Programming Prerequisite", *Journal of Engineering Education*, April 1966, pp. 313-315.
- 7. Parnas, D.L., "On the Preliminary Report of C<sup>3</sup>S", (Letter to the Editor), *Communications of the ACM*, 9, 4, April 1966, pp. 242-243.
- 8. Parnas, D.L., "On Facilitating Parallel and Multi-Processing in Algol", (Letter to the Editor), *Communications of the ACM*, 9, 4, April 1966, p. 257.
- 9. Parnas, D.L., Richardson, L.C., Kohl, W.H., "Preliminary Version--An Introduction to Boole-66", Computation Center, Carnegie -Mellon University, Pittsburgh PA., September 1966, 23 pgs.
- Parnas, D.L., "State Table Analysis of Programs in an Algol-Like Language", Proceedings of the 1966 National ACM Conference, Thompson Book Co., Washington, D.C., pp. 391-400.
- 11. Parnas, D.L., "Sequential Equivalents of Parallel Processes", Carnegie Institute of Technology, Pittsburgh PA., February 1967, 38 pgs.
- 12. Parnas, D.L., Darringer, J.A., "SODAS and a Methodology for System Design", Proceedings of AFIPS 1967 Fall Joint Computing Conference, pp. 449-474.
- 13. Parnas, D.L., "On Improving the Quality of Our Technical Meetings", (Letter to the Editor), *Communications of the ACM*, 11, 8, September 1968, pp. 537.
- 14. Parnas, D.L., "On Operating Computer Systems and Simulation Systems", *Computer Science Research Review 3*, Carnegie-Mellon University, December 1968, pp. 23-27.
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- Parnas, D.L., "The Application of Modelling to System Development and Design", *International Computing Symposium*, ACM European Chapters, Vol. IV, May 1970, pp. 137-147.
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  - Also in *Software Specification Techniques* edited by N. Gehani & A.D. McGettrick, AT&T Bell Telephone Laboratories, Addison Welsey, 1985, pp. 75-88 (QA 76.7 S6437).
  - Translated into Russian book "*Danniye v yazikach programmirovania*" Moscow, Mir (Publishing House), 1984, pp. 9-24.
- 25. Parnas, D.L., Habermann, A.N., "Comment on Deadlock Prevention Method", *Communications of the ACM*, 15, 9, September 1972, pp. 840-841.
- 26. Parnas, D.L., "On the Criteria to be Used in Decomposing Systems into Modules", *Communications of the ACM*, 15, 12, December 1972, pp. 1053-1058.
  - Translated into Japanese *BIT*, vol. 14, no. 3, 1982, pp. 54-60.
  - Republished in *Classics in Software Engineering*, edited by Edward Nash Yourdon, Yourdon Press, 1979, pp. 141-150.
  - Republished in *Great Papers in Computer Science*, edited by Phillip Laplante, West Publishing Co, Minneapolis/St. Paul 1996, pp. 433-441.
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- 27. Parnas, D.L., "Sample Man Machine Interface Specification--A Graphics Based Line Editor", In *Display Use for Man-Machine Dialog* (eds. W. Handler, J. Weizembaum), Carl Hanser Verlag, Munchen, 1972, pp. 33-52.
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- 29. Courtois, P.J., Heymans, F., Parnas, D.L., "Comments on a Comparison of Two Synchronizing Concepts", *Acta Informatica*, 1, 1972, pp. 375-376.
- 30. Parnas, D.L., "Some Conclusions from an Experiment in Software Engineering Techniques", *Proceedings of the 1972 FJCC*, 41, Part I, pp. 325-330.
- 31. Parnas, D.L., Price, W.R., "The Design of the Virtual Memory Aspects of a Virtual Machine", *Proceedings of the ACM SIGARCH-SIGOPS Workshop on Virtual Computer Systems*, March 1973, 7 pgs.
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- 35. Parnas, D.L., Price, W.R., "Design of a Non-Random Access Virtual Memory Machine", Proceedings of the International Workshop On Protection in Operating Systems, Paris, August 1974, 14 pgs.
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- 37. Parnas, D.L., "Review of A Comparison of Two Synchronizing Concepts", (Brinch Hansen, Per., *Acta Informatica 1, 3), Computing Reviews*, 15, 6, June 1974, pp. 203-204.
- 38. Cooprider, L.W., Courtois, P.J., Heymans, F., Parnas, D.L., "Information Streams Sharing a Finite Buffer: Other Solutions", *Information Processing Letters*, 3, 1, July 1974, pp. 16-21.
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- 52. Parnas, D.L., Bartussek, W., Handzel, G., Würges, H., "Using Predicate Transformers to Verify the Effects of 'Real' Programs", UNC Report No. TR-76-101, Oct. 1976, 36 pgs.
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- 54. Parnas, D.L., "Building Reliable Software in Blowhard", *ACM Software Engineering Notes*, 2, 3, April 1977, pp. 5-6.
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- 56. Parnas, D.L., "Further Excerpts from the ACM SIGTRANS Notices", *ACM Software Engineering Notes*, 2, 5, October 1977, pp. 6-7.
- 57. Bartussek, W., Parnas, D.L., "Using Assertions About Traces to Write Abstract Specifications for Software Modules", UNC Report No. TR77-012, Dec. 1977, 26 pgs.
  - Also in Lecture Notes in Computer Science (65), *Information Systems Methodology*, *Proceedings ICS*, Venice, 1978, Springer Verlag, pp. 211-236.
  - Also in *Software Specification Techniques* edited by N. Gehani & A.D. McGettrick, AT&T Bell Telephone Laboratories, 1985, pp. 111-130 (QA 76.6 S6437).
  - Reprinted as Chapter 1 in item 222.
- 58. Parnas, D.L., "Use of Abstract Interfaces in the Development of Software for Embedded Computer Systems", NRL Report No. 8047, June 1977, 30 pgs.
  - Reprinted in Infotech State of the Art Report, Structured System Development, Infotech International, 1979.
- 59. Parnas D.L., "Some Software Engineering Principles", Infotech State of the Art Report on Structured Analysis and Design, *Infotech International*, 1978, 10 pgs.
  - Reprinted as Chapter 13 in item 222.
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  - Also in *Proceedings of the Third International Conference on Software Engineering*, May 1978, pp. 264-277.
  - Reprinted as Chapter 14 in item 222
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- 62. Parnas, D.L., "The Non-Problem of Nested Monitor Calls", *ACM Operating Systems Review*, 12, 1, January 1978, pp. 12-14.
- 63. Parnas, D.L., "Another View of the Dijkstra-DMLP Controversy", *ACM Software Engineering Notes*, 3, 4, October 1978, pp. 20-21.

- 64. Heninger, K., Kallander, J., Parnas, D.L., Shore, J., "Software Requirements for the A-7E Aircraft", NRL Report 3876, November 1978, 523 pgs.
- 65. Parnas, D.L., Review of "Measuring Improvements in Program Clarity" by R.D. Gordon, (*IEEE Transactions Software Engineering*, SE-5, 2, March 1979, pp. 79-90) in *Computing Reviews*, 21, 1, January 1980, p. 31.
- 66. Parnas, D.L., "Research Problems in Programming Methodology, In Research Directions in Software Technology", Peter Wegner (Ed.), The MIT Press, 1979, pp. 352-385.
- 67. Parnas, D.L., "The Role of Program Specifications", in *Research Directions in Software Technology*, Peter Wegner (Ed.), The MIT Press, 1979, pp. 364-370.
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- 73. Britton, K.H., Clements, P., Parnas, D.L., "Interface Specifications for the SCR (A-7E) Extended Computer Module", NRL Report 4843, Jan. 1983. (revised as item 86).
- 74. Parnas D.L., "Software Engineering Principles", University of Victoria Report No. DCS-29-IR, February 1983, 14 pgs.
- 75. Clements P.C., Parnas, D.L., Weiss, D.M., "Enhancing Reusability with Information Hiding", *Proceedings of the Workshop on Reusability in Programming*, September 1983, pp. 240-247. (revised as item 103)
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- Faulk, S.R., Parnas, D.L., "On the Uses of Synchronization in Hard-Real-Time Systems", *Proceedings of Real-Time Systems Symposium*, Arlington, Virginia, 6-8 December 1983, pp. 101-109, IEEE Cat. No. 83CH1941-4.

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     Also a partial translation "Wie Kunstliche Blumen", in Frontal, May/June 1986, p. 46.
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